



LESSON PLAN FOR Analog Electronics Practical (pp2)

Discipline: Electrical Engineering	Semester: 4th	Name of the Teaching Faculty: Sri Mahesh kumar biswal
Subject: Analog Electronics Lab	No. of days per week class allotted: 3	Semester From Date : 04/02/2025 to Date: 17/05/2025 No. of Weeks: 15
Week	Class Day	Practical Topic
1st	1st	introduction to context
	2nd	Determine the input and output Characteristics of CE & CB transistor configuration
	3rd	Determine the input and output Characteristics of CE & CB transistor configuration
2nd	1st	Determine Drain & Transfer Characteristics of JFE
	2nd	Determine Drain & Transfer Characteristics of JFE
	3rd	Determine Drain & Transfer Characteristics of JFE
3rd	1st	. Construct Bridge Rectifier using different filter circuit and to determine Ripple factor & analyze wave form with filter & without filter.
	2nd	. Construct Bridge Rectifier using different filter circuit and to determine Ripple factor & analyze wave form with filter & without filter.
	3rd	. Construct Bridge Rectifier using different filter circuit and to determine Ripple factor & analyze wave form with filter & without filter.
4th	1st	Construct Bridge Rectifier using different filter and to determine Ripple factor
	2nd	Construct Bridge Rectifier using different filter and to determine Ripple factor
	3rd	Construct Bridge Rectifier using different filter and to determine Ripple factor
5th	1st	Construct & test the regulator using Zener diode
	2nd	Construct & test the regulator using Zener diode
	3rd	Construct & test the regulator using Zener diode
6th	1st	Construct different types of biasing circuit and analyze the wave form (i) Fixed bias (ii) Emitter bias
	2nd	Construct different types of biasing circuit and analyze the wave form (i) Fixed bias (ii) Emitter bias
	3rd	Construct different types of biasing circuit and analyze the wave form (i) Fixed bias (ii) Emitter bias
7th	1st	Study the single stage CE amplifier & find Gain
	2nd	Study the single stage CE amplifier & find Gain
	3rd	Study the single stage CE amplifier & find Gain
8th	1st	Study multi stage R-C coupled amplifier & to determine frequency- response & gain.
	2nd	Study multi stage R-C coupled amplifier & to determine frequency- response & gain.
	3rd	Study multi stage R-C coupled amplifier & to determine frequency- response & gain.
9th	1st	Construct & Find the gain (I) Class A. Amplifier
	2nd	Construct & Find the gain (I) Class A. Amplifier
	3rd	Construct & Find the gain (I) Class A. Amplifier
	1st	Construct & test push pull amplifier & observer the wave form

10th	2nd	Construct & test push pull amplifier & observe the wave form
	3rd	Construct & test push pull amplifier & observe the wave form
11th	1st	Construct & calculate the frequency of (i) Hartly Oscillator (ii) Collpit's Oscillator (iii) Wein Bridge Oscillator (iv) R-C phas
	2nd	Construct & calculate the frequency of (i) Hartly Oscillator (ii) Collpit's Oscillator (iii) Wein Bridge Oscillator (iv) R-C phas
	3rd	Construct & calculate the frequency of (i) Hartly Oscillator (ii) Collpit's Oscillator (iii) Wein Bridge Oscillator (iv) R-C phas
12th	1st	Construct & Find the gain (ii) Class B. Amplifier (iii) Class C Tuned Amplifier
	2nd	Construct & Find the gain (ii) Class B. Amplifier (iii) Class C Tuned Amplifier
	3rd	Construct & Find the gain (ii) Class B. Amplifier (iii) Class C Tuned Amplifier
13th	1st	Construct & Test Differentiator and Integrator using R-C Circuit
	2nd	Construct & Test Differentiator and Integrator using R-C Circuit
	3rd	Construct & Test Differentiator and Integrator using R-C Circuit
14th	1st	Study Multivibrator (Astable, Bistable, Monstable) Circuit & Draw its Wave form
	2nd	Study Multivibrator (Astable, Bistable, Monstable) Circuit & Draw its Wave form
	3rd	Study Multivibrator (Astable, Bistable, Monstable) Circuit & Draw its Wave form
15th	1st	Construct different types of biasing circuit and analyze the wave (iii) Voltage divider bias
	2nd	Construct different types of biasing circuit and analyze the wave (iii) Voltage divider bias
	3rd	Construct different types of biasing circuit and analyze the wave (iii) Voltage divider bias

signature of
HOD(electrical)

signature of academic coordinator

signature of course coordinator